APR 14 2008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.:	10/689,365
Applicant:	BEAUMONT, Mark
Filed:	20 October 2003
Title:	Method For Load Balancing an N-Dimensional Array of Parallel Processing Elements
Art Unit:	2195
Examiner:	KAWSAR, Abdullah Al
Docket No.:	PAT001062-000

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

OK TO ENTER
/04/24/08
/A.K./

Enclosed in connection with the above-referenced application is the following:

- Amendment Under Rule 312

The Commissioner is hereby authorized to charge any fees due in connection with this filing to our Deposit Account No. 50-3013. A copy of this Transmittal Letter is enclosed.

Respectfully submitted,

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Date: 11 April 2008

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